

MIU 14.4 SLEEP MODE SETTINGS

AUTO RESET

Dip switch ONE (DIP Switch next to U7/J1) will allow the modem to be automatic reconfigure (RESET) to store profile 0 setting. If the modem original configuration setting is lost for any reason, it can be restored by dialing the modem. Allowing the modem to ring 3 to 6 times that will cause a reset modem condition.

POWER ON

Dip switch two on position is two U5

Set the modem Power features for always ON or Power down mode (OFF position) when modem is in the idle Mode. Factory setting is ON (Power is always available)

AUTO POWER ON/OFF (Typically only Auto Power or Sleep Mode should be ON)

Dip switch three ON position to U5

When switch three is in the ON position it will set the modem for Auto Power Down mode. In the power down mode, after 30 seconds (not adjustable) of any activity, power will reduce the current to 20uA.

No activity is modem off line and No TxD.

The modem will automatically turn on to full power within 400ms of receiving a DTR signal (OFF to ON) transition or incoming ring signal is detected.

SLEEP MODE (Typically only Auto Power or Sleep Mode should be ON)

DIP switch four ON position to U5

In the Sleep Mode when there is no DTR or detected telephone line activity as pre-determined by S37 (default value is 50 seconds), the modem will enter the sleep mode. Power will be reduce to 6mA (approximately) in the sleep mode. When a ring signal is detected or DTR is switched from OFF to ON positions the modem will be activated between 200ms to 400ms S37 range is 0.0 to 250 seconds

Note: 1- When using the PEXXXX with the MIU (Input voltage of 48VAC/DC to 220VAc/DC) power supply there will be very little power saving as the MIU unit cannot adjust for low current drain.

2- In the Sleep Mode all RS-232 output signals are OFF (at zero voltage) RS-232 transmit circuits is also shut down as a power saving features.

3- In the Auto Power or Sleep Mode when a ring signal is detected the modem will wake up from the power reduction mode. To answer a call the S0 register must still be set for one or higher value.

